

Application No. 10730167 (Docket: CNTR.2224-C1)
37 CFR 1.111 Amendment dated 08/04/2007
Reply to Office Action of 04/23/2007

AMENDMENTS TO THE ABSTRACT

Please delete the section entitled "ABSTRACT OF THE DISCLOSURE" in its entirety and substitute the following section therefor:

ABSTRACT OF THE DISCLOSURE

A microprocessor apparatus is provided, for performing a cryptographic operation. The microprocessor apparatus includes fetch logic that is configured to fetch an instruction flow from memory for execution by a microprocessor. The instruction flow includes an instruction that directs the microprocessor to perform the cryptographic operation. The instruction has an opcode field and a repeat prefix field. The opcode field prescribes that the device accomplish the cryptographic operation as further specified within a control word stored in a memory. The repeat prefix field is coupled to the opcode field. The repeat prefix field indicates that the cryptographic operation prescribed by the instruction is to be accomplished on a plurality of blocks of input data.
~~The present invention provides an instruction for employment by a device. The instruction directs the device to perform a cryptographic operation. The instruction has an opcode field and a repeat prefix field. The opcode field prescribes that the device accomplish the cryptographic operation as further specified within a control word stored in a memory. The repeat prefix field is coupled to the opcode field. The repeat prefix field indicates that the cryptographic operation prescribed by the instruction is to be accomplished on a plurality of blocks of input data.~~